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Sherrod

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[54]	METHOD AND APPARATUS FOR SCHEDULING THE EXECUTION OF
	MULTIPLE PROCESSING TASKS IN A COMPUTER SYSTEM

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References Cited [56] U.S. PATENT DOCUMENTS

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ABSTRACT

A task schedular for scheduling the execution of a plurality of tasks within a computer system. The task scheduler utilizes a combination of externally assigned priorities and internally calculated priorities to optimize the responsiveness of the computer to external interac-

4 Claims, 4 Drawing Figures

